

LOGGER ROM CODING

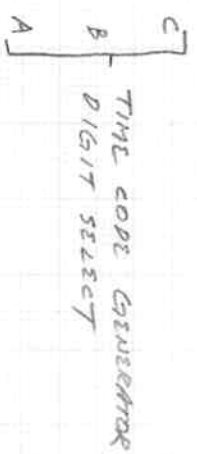
DECIMAL ADDRESS	OCTAL ADDRESS	CODE FUNCTION	HEX	ROM OUTPUT								
				07 1	06 2 LSD	05 4	04 8	03 1	02 2 MSD	01 4	08 8	
0	00	CR	0D	1	0	1	1	0	0	0	0	0
1	01	LF	0A	0	1	0	1	0	0	0	0	0
2	02	H10	3F	1	1	1	1	1	1	1	1	1
3	03	H1	3F	1	1	1	1	1	1	1	1	1
4	04	COLON	3A	0	1	0	1	1	1	1	1	1
5	05	M10	3F	1	1	1	1	1	1	1	1	1
6	06	M1	3F	1	1	1	1	1	1	1	1	1
7	07	COLON	3A	0	1	0	1	1	1	1	1	1
8	10	S10	3F	1	1	1	1	1	1	1	1	1
9	11	S1	3F	1	1	1	1	1	1	1	1	1
10	12	SPACE	20	0	0	0	0	0	1	1	1	1
11	13	ALARM	6F	1	1	1	1	0	1	1	1	1
12	14	SPACE	20	0	0	0	0	0	1	1	1	1
13	15	AUDIOENABLE	FF	1	1	1	1	1	1	1	1	1

P/N 3131-6330

* IN CIRCUIT, BIT B IS IGNORED, SO IT IS LEFT IN ITS NATURALLY OCCURRING STATE!

DECIMAL ADDRESS	OCTAL ADDRESS	INSTRUCTION FUNCTION	HEX	ROM OUTPUT								
				01 1	02 2 LSD	03 4	04 8	05 1	06 2 MSD	07 4	08 8	
0	00	CR	F7	1	1	1	0	1	1	1	1	1
1	01	LF	F7	1	1	1	0	1	1	1	1	1
2	02	H10	F0	0	0	0	0	1	1	1	1	1
3	03	H1	F1	1	0	0	0	1	1	1	1	1
4	04	COLON	F7	1	1	1	0	1	1	1	1	1
5	05	M10	F2	0	1	0	0	1	1	1	1	1
6	06	M1	F3	1	1	0	0	1	1	1	1	1
7	07	COLON	F7	1	1	1	0	1	1	1	1	1
8	10	S10	F4	0	0	1	0	1	1	1	1	1
9	11	S1	F5	1	0	1	0	1	1	1	1	1
10	12	SPACE	F7	1	1	1	0	1	1	1	1	1
11	13	ALARM	FF	1	1	1	1	1	1	1	1	1
12	14	SPACE	F7	1	1	1	0	1	1	1	1	1
13	15	AUDIOENABLE	F7	1	1	1	0	1	1	1	1	1

P/N 3132-6330



ALARM ENABLE

A-013