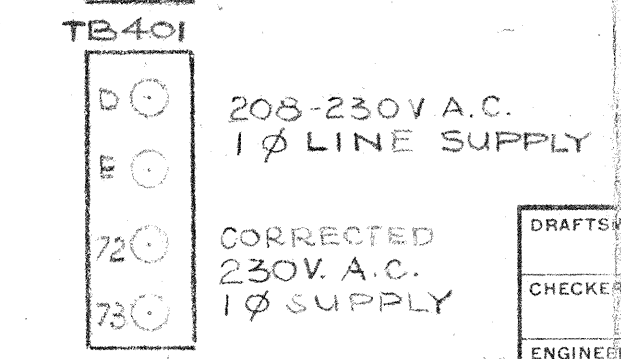
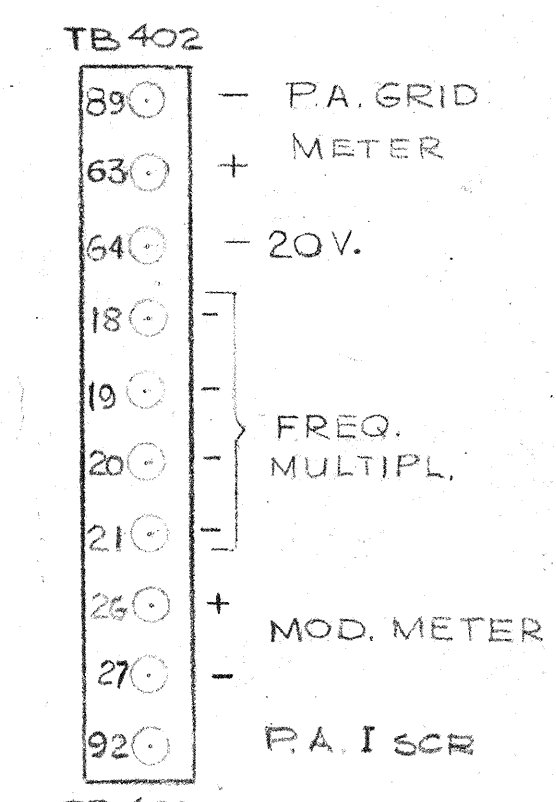
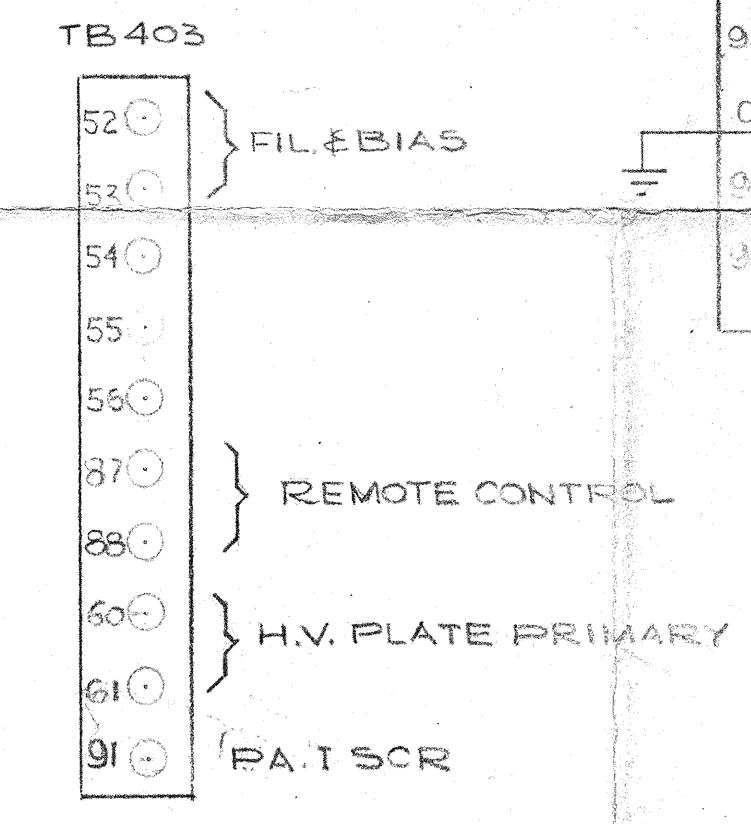
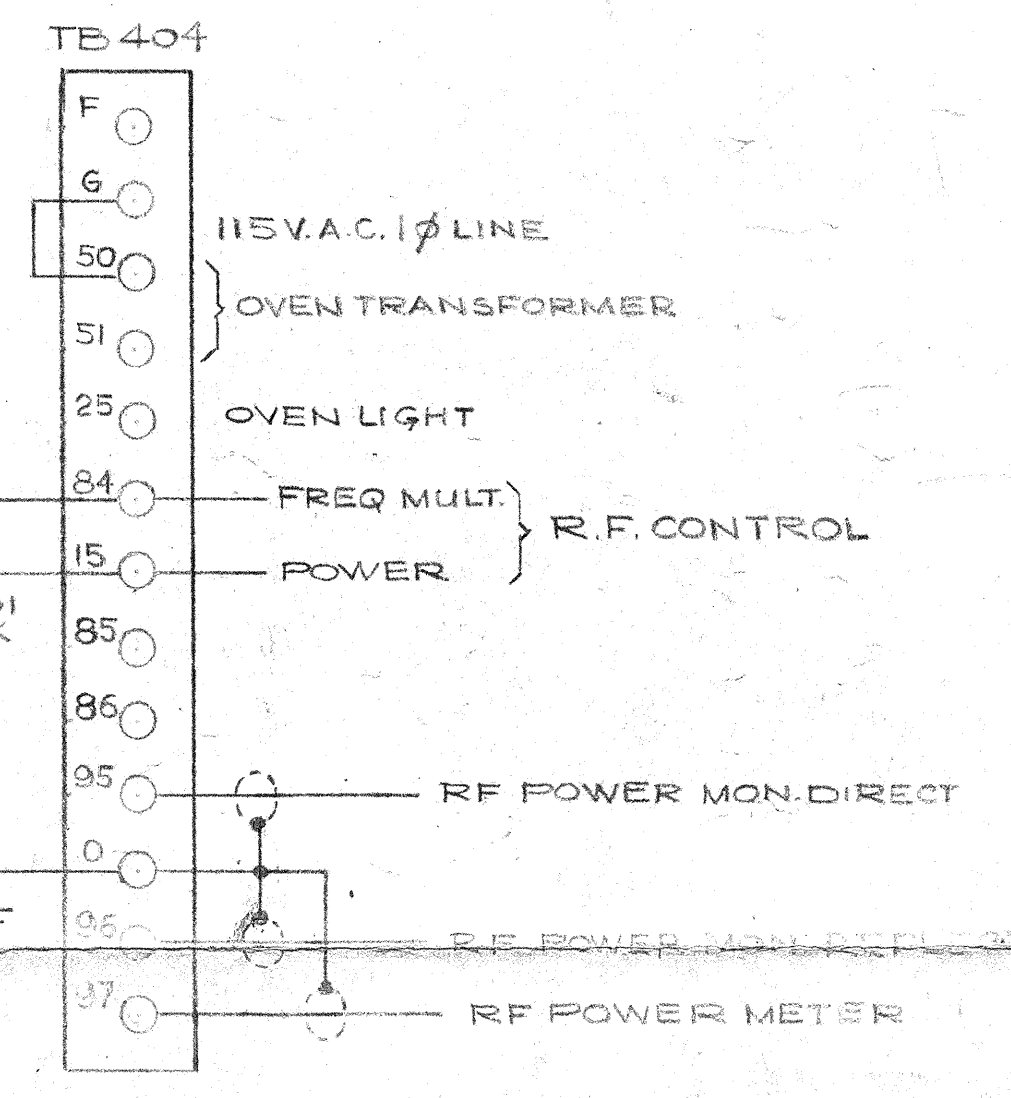
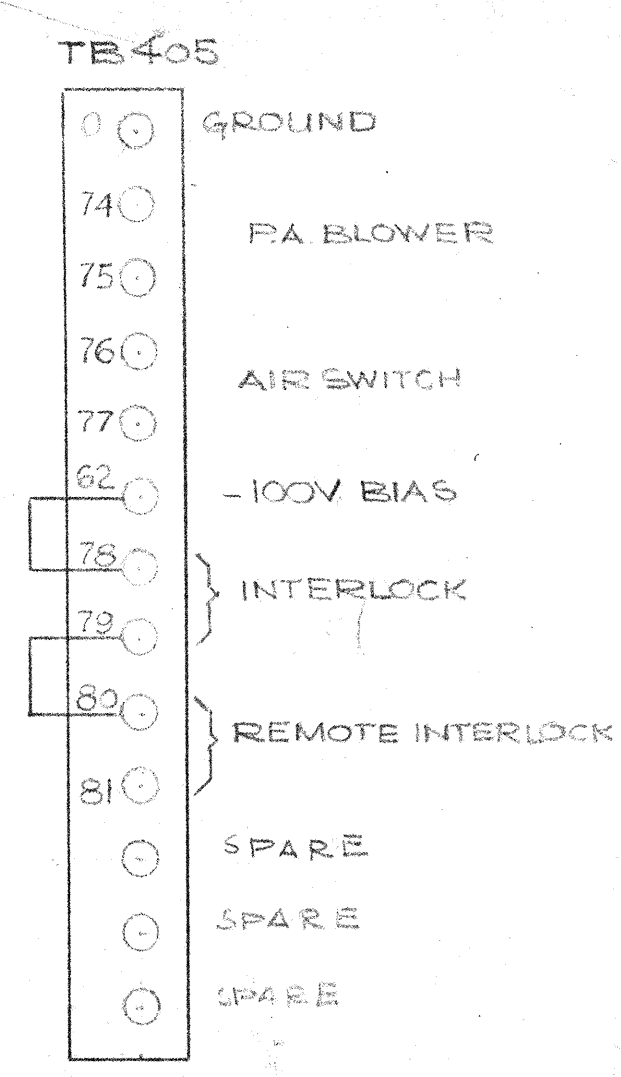
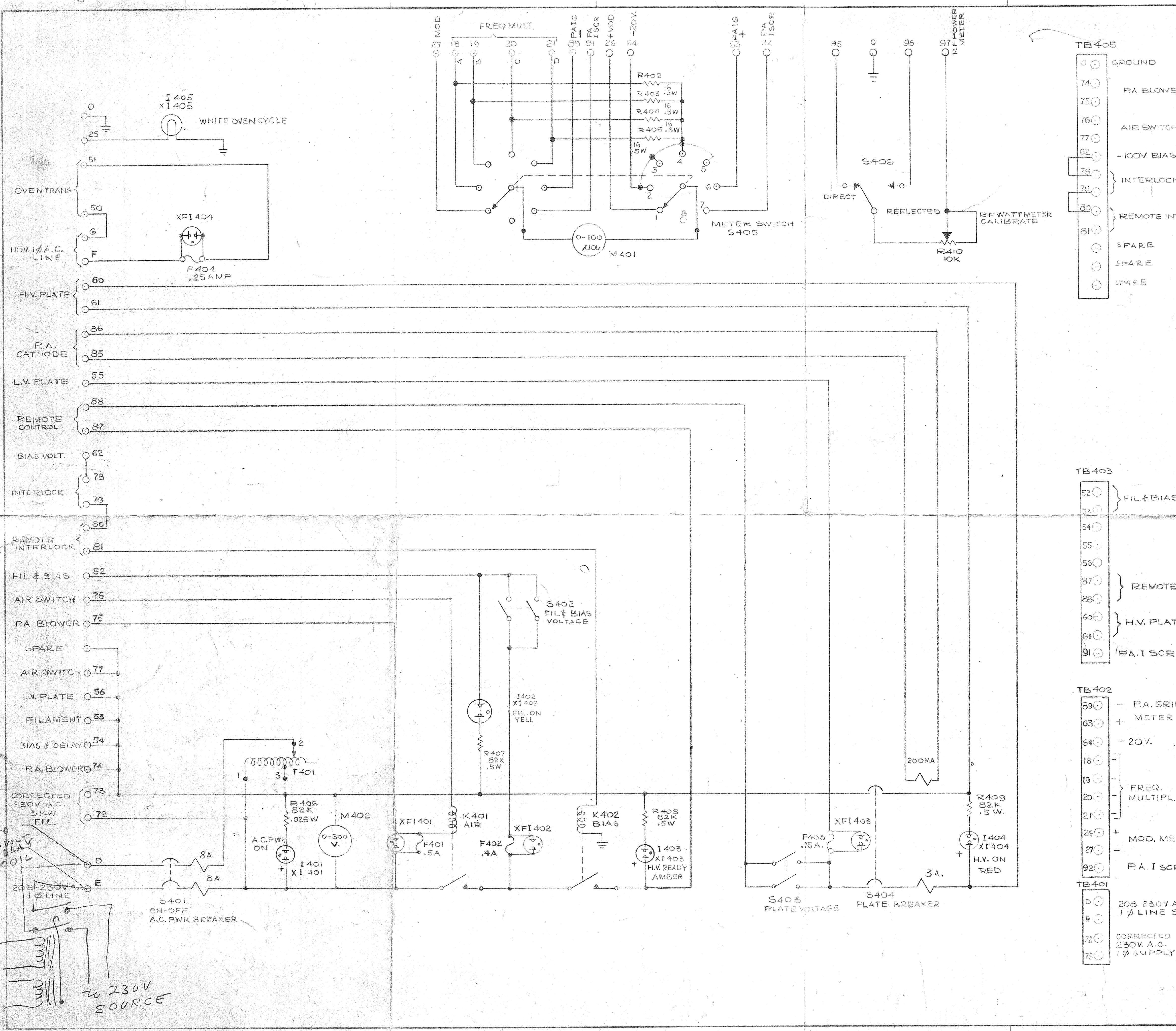


| ZONE | REV. | REVISION DESCRIPTION | DATE | APPROVAL |
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| DRAFTSMAN AZWCLD | DATE 2/10/59 | NOMENCLATURE AND DRAWING TITLE CONTROL PANEL SCHEMATIC DIAGRAM S-1790 | STANDARD ELECTRONICS A DIVISION OF RADIO ENGINEERING LABORATORIES INC. 29-01 BORDEN AVE. LONG ISLAND CITY 1, N.Y. |
| CHECKER | | | |
| ENGINEER | | | |
| APPROVED | | | |
| SCALE | WEIGHT | DWG. NO. E-1790-0-00 | |
| | | SHEET 1 OF SHEETS | |